Your guide to the world of microprocessors.

The Micro-Professor™-A low cost tool for learning, teaching and prototyping.

Here in one attractive package and at a price of only \$149.00 is a Z80* based microcomputer to lead you step by step to a thorough knowledge of the world of microprocessors.

The Micro-Professor is a complete hardware and software system whose extensive teaching

* Z80 is a trademark of Zilog Inc.

manual gives you detailed schematics and examples of program code. A superb learning tool for students, hobbyists and microprocessor enthusiasts, as well as an excellent teaching aid for instructors of electrical engineering and computer science courses.

But the Micro-Professor is much more than a teaching device. With it you can do breadboarding and prototyping, designing your own custom hardware and software applications with Z80, 8080 and 8085 compatible code.

The standard 2K bytes of RAM is expandable to 4K, and the standard 2K bytes of ROM can be increased to 8K.

All this plus a built-in speaker, a cassette interface, and

EPB-MPF EPROM Programming Board \$169

sockets to accept optional

CTC/PIO. Bus is extendable.

As well as being an exciting

learning tool, the Micro-Professor

For all +5V 1KB/2KB/ 4KB EPROMs.

Read/Copy/List/Verify Capability.

BASIC-MPF Tiny Basic \$19

2KB BASIC interpreter with hardware control capability. Machine-code subroutine accessible.

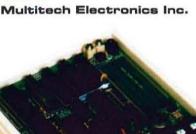
is a great low-cost board for OEM's. Call for details.

SSB-MPF Speech Synthesizer Board \$129

vocabulary of up to 400 words based on the TMS 5200 chip.

State







I'm ready to enter the world of microprocessing

Call toll free to order.

	Price	Qty.	Amoun
MPF-I Micro-Professor	\$149.00		
SSB-MPF Speech Synthesizer Board	\$129.00		
EPB-MPF EPROM Programming Board	\$169.00		
BASIC-MPF	\$ 19.00		
Shipping and Handling	\$ 4.95		4.95
California resident	s add sales	tax.	
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In U.S. and Canada mail to:

Multitech Electronics Inc. 195 West El Camino Real Sunnyvale, CA 94086 California Tel. (408) 773-8400 Elsewhere (800) 538-1542

Outside of North America mail to:

Multitech Industrial Corporation 977 Min Shen E Road, 105 Taipei, Taiwan, ROC Tel. 02-769-1225 TWX 19162 MULTIIC, 23756 MULTIIC

Features and Specifications

MPF-1

CPU Z80 CPU high performance microprocessor with 158 instructions.

RAM 2K bytes expandable to 4K bytes.

ROM 2K bytes of sophisticated monitor expandable to 8K bytes.

Input/Output 24 system I/O lines.

Monitor 2K bytes of sophisticated monitor. Monitor includes system initialization, keyboard scan, display scan, tape write and tape read.

Display 6-digit, 0.5" red LED display.

Audio Cassette Interface 165-bit/sec average rate for data transfer between memory and cassette tape.

Extension Connectors All buses of CPU, channel signals of CTC, and I/O port bus of PIO usable for expansion.

Counter Timer Circuit Socket is provided.

Parallel I/O Circuit Socket is provided.

Speaker & Speaker 2.5" diameter speaker **Driver Circuits**

User Area Provides a 3.5" × 1.36" wire wrapping area for user's expansion.

Power Requirement 9V, 0.5A adaptor is provided.

User's Manual Complete self-learning text with experiments and applications.

Keyboard 36 keys including 19 function keys, 16 hex-digit keys, and 1 user-defined key.

RS	Reset the system.	GO	Execute the user's program.	
ADDR	Set memory address and display content.	INS	Insert data of the address fol- lowed by the current display	
DATA	Input data to memory or register.		address.	
PC	Recall program counter.	DEL	Delete data of the current display address.	
REG	Select register and display con- tents of register.	MOVE	Move memory block in the RAM.	
+	Display content of next memory or register.	RELA	Relative address calculation. Calculates and stores relative address.	
_	Display content of last memory address or register.	TAPE WR	Store data to the cassette tape.	
STEP	Single step execution of user's program.	TAPE RD	Load data from the recorder.	
		INTR	Maskable interrupt.	
SBR	Set break point of user's	USER KEY	KEY User defined key.	
	program.	O-F	Hex-digits or register selection.	
MONI	User's program break and return		Control of the Contro	

EPB-MPF Specifications

Hardware Specifications

Compatible with MPF-1. Use 40-pin flat ribbon cable and male connector to interface with MPF-1.

ROM: Single + 5V EPROM 2516 \times 1. Total of 2K bytes. Monitor EPROM address: 9000-97FF.

RAM: Static RAM, 6116 × 2. Total of 4K bytes. Basic RAM address: 8000-8FFF.

I/O Port: Programmable I/O port, 3255 × 1. Total of 24 parallel I/O lines. I/O address: CC-CF

System Power Consumption: 25V/ 30mA and 5V/350mA

Main Power Input: 30V/75mA and 9V/400mA adaptor is provided. Power adaptor input 110V.

Textool: 24-pin, zero insertion force socket.

Software Specifications

to monitor.

READ: Read data from EPROM onto RAM buffer.

VERIFY: Verify EPROM data with RAM buffer.

LIST: Display or modify data on RAM buffer.

RESTART: Restart to initial state of EPB-MPF.

PROGM: Write data from RAM buffer to EPROM.

DEL: Delete data from the current display address in RAM buffer.

INS: Insert data onto the address followed by the current display address of RAM BUFFER.

SSB-MPF Features

- · Uses high reliability TI speech synthesis chip.
- · 4KB EPROM for time-clock program and speech utility.
- Two EPROM sockets for expanding speech vocabulary.
- · Shares the Z80 CPU of MPF-1 as host controller.
- · Uses keyboard and speaker of MPF-1 as input/output device.
- Adjustable voice pitch and volume.
- · 9V, 0.5A adaptor is provided.
- · Complete accessories including 40-pin, double-headed connector, audio jumper, operation manual, etc.



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